

532 Rec'd PCT/PTC 29 AUG 2000

TRANSMITTAL LETTER TO THE UNITED STATES
DESIGNATED/ELECTED OFFICE (DO/EO/US)
CONCERNING A FILING UNDER 35 U.S.C. 371

104-279P

U.S. APPLICATION NO. (If known, see 37 CFR 1.5)

09/623195

INTERNATIONAL APPLICATION NO.

PCT/SE99/00310

INTERNATIONAL FILING DATE

March 2, 1999

PRIORITY DATE CLAIMED

March 2, 1998

TITLE OF INVENTION

IMPROVED PATTERN GENERATOR

APPLICANT(S) FOR DO/EO/US

SANDSTROM, Torbjorn

Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:

1. ☒ This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371.
2. ☐ This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S.C. 371.
3. ☒ This express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39 (1).
4. ☒ A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date
5. ☒ A copy of the International Application as filed (35 U.S.C. 371(c)(2))
- a. ☒ is transmitted herewith (required only if not transmitted by the International Bureau). WO 99/45439
- b. ☒ has been transmitted by the International Bureau.
- c. ☐ is not required, as the application was filed in the United States Receiving Office (RO/US).
6. ☒ A translation of the International Application into English (35 U.S.C. 371(c)(3)).
7. ☒ Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(2)).
- a. ☐ are transmitted herewith (required only if not transmitted by the International Bureau).
- b. ☐ have been transmitted by the International Bureau.
- c. ☐ have not been made; however, the time limit for making such amendments has NOT expired.
- d. ☒ have not been made and will not be made.
8. ☐ A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).
9. ☒ An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)).
10. ☐ A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)).

Items 11. to 16. below concern document(s) or information included:

11. ☒ An Information Disclosure Statement under 37 CFR 1.97 and 1.98.-1449 and International Search Report (PCT/ISA/210) w/ 1 reference
12. ☒ An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.
13. ☒ A FIRST preliminary amendment.
☐ A SECOND or SUBSEQUENT preliminary amendment.
14. ☐ A substitute specification.
15. ☐ A change of power of attorney and/or address letter.
16. ☒ Other items or information:
1.) Small Entity Statement
2.) International Preliminary Examination Report (PCT/IPEA/409)
3.) Eight (8) sheets of Formal Drawings

U.S. APPLICATION NO (if known, see 37 CFR 1.5) <div style="font-size: 24pt; font-weight: bold;">09/623195</div>		INTERNATIONAL APPLICATION NO PCT/SE99/00310		<div style="text-align: right;"> 428 Rec'd PCT/PTO 29 AUG 2000 </div> ATTORNEY'S DOCKET NUMBER 104-279P	
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17. ☒ The following fees are submitted:

BASIC NATIONAL FEE (37 CFR 1.492(a)(1)-(5):
 Neither international preliminary examination fee (37 CFR 1.482)
 nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO
 and International Search Report not prepared by the EPO or JPO. **\$970.00**

International preliminary examination fee (37 CFR 1.482) not paid to
 USPTO but International Search Report prepared by the EPO or JPO **\$840.00**

International preliminary examination fee (37 CFR 1.482) not paid to USPTO
 but international search fee (37 CFR 1.445(a)(2)) paid to USPTO. **\$690.00**

International preliminary examination fee (37 CFR 1.482) paid to USPTO
 but all claims did not satisfy provisions of PCT Article 33(1)-(4) **\$670.00**

International preliminary examination fee (37 CFR 1.482) paid to USPTO
 and all claims satisfied provisions of PCT Article 33(1)-(4). **\$96.00**

ENTER APPROPRIATE BASIC FEE AMOUNT =

Surcharge of **\$130.00** for furnishing the oath or declaration later than ☐ 20 ☐ 30
 months from the earliest claimed priority date (37 CFR 1.492(e)).

CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE		
Total Claims	37 - 20 =	17	X \$18.00	\$	306.00
Independent Claims	1 - 3 =	0	X \$78.00	\$	0
MULTIPLE DEPENDENT CLAIM(S) (if applicable)			None	+ \$260.00	\$ 0
TOTAL OF ABOVE CALCULATIONS =				\$	1276.00
Reduction of 1/2 for filing by small entity, if applicable. Verified Small Entity statement must also be filed (Note 37 CFR 1.9, 1.27, 1.28).				\$	-638.00
SUBTOTAL =				\$	638.00
Processing fee of \$130.00 for furnishing the English translation later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(f)).				\$	0
TOTAL NATIONAL FEE =				\$	638.00
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31). \$40.00 per property +				\$	40.00
TOTAL FEES ENCLOSED =				\$	678.00
				Amount to be: refunded	\$
				charged	\$

a. ☒ A check in the amount of \$ **678.00** to cover the above fees is enclosed.

b. ☐ Please charge my Deposit Account. No. _____ in the amount of \$ _____ to cover the above fees.
A duplicate copy of this sheet is enclosed.

c. ☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any
overpayment to Deposit Account No. 02-2448.

NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.

Send all correspondence to:
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SIGNATURE
 SLATTERY, JAMES M.
 NAME
 #28,380 (JMS)
 REGISTRATION NUMBER

/cqc August 29, 2000

STATEMENT CLAIMING SMALL ENTITY STATUS
(37 CFR 1.9(f) & 1.27(c)) - SMALL BUSINESS CONCERN

Docket Number: 104-279P

Applicant, Patentee, or Identifier: Torbjorn SANDTROM

Application or Patent No.: NEW

Filed or Issued: AUGUST 29, 2000

Title: IMPROVED PATTERN GENERATOR

I hereby state that I am

- ☐ the owner of the small business concern identified below:
☐ an official of the small business concern empowered to act on behalf of the concern identified below:

NAME OF SMALL BUSINESS CONCERN MICRONIC LASER SYSTEMS AB

ADDRESS OF SMALL BUSINESS CONCERN P.O. Box 3141, SE-183 03 TABY SWEDEN

I hereby state that the above identified small business concern qualifies as a small business concern as defined in 37 CFR Part 121 for purposes of paying reduced fees to the United States Patent and Trademark Office, in that the number of employees of the concern, including those of its affiliates, does not exceed 500 persons. For purposes of this statement, (1) the number of employees of the business concern is the average over the previous fiscal year of the concern of the persons employed on a full-time, part-time, or temporary basis during each of the pay periods of the fiscal year, and (2) concerns are affiliates of each other when either, directly or indirectly, one concern controls or has the power to control the other, or a third party or parties controls or has the power to control both.

I hereby state that rights under contract or law have been conveyed to and remain with the small business concern identified above with regard to the invention described in:

- ☐ the specification filed herewith with title as listed above.
☐ the application identified above.
☐ the patent identified above.

If the rights held by the above identified small business concern are not exclusive, each individual, concern, or organization having rights in the invention must file separate statements as to their status as small entities, and no rights to the invention are held by any person, other than the inventor, who would not qualify as an independent inventor under 37 CFR 1.9(c) if that person made the invention, or by any concern which would not qualify as a small business concern under 37 CFR 1.9(d), or a nonprofit organization under 37 CFR 1.9(e).

Each person, concern, or organization having any rights in the invention is listed below:

- ☐ no such person, concern, or organization exists.
☐ each such person, concern, or organization is listed below.

Separate statements are required from each named person, concern, or organization having rights to the invention stating their status as small entities. (37 CFR 1.27)

I acknowledge the duty to file, in this application or patent, notification of any change in status resulting in loss of entitlement to small entity status prior to paying, or at the time of paying, the earliest of the issue fee or any maintenance fee due after the date on which status as a small entity is not longer appropriate. (37 CFR 1.28(b))

NAME OF PERSON SIGNING Bert Jeppsson

TITLE IN ORGANIZATION OF PERSON SIGNING President & CEO

ADDRESS OF PERSON SIGNING Box 3141 SE 183 03 Taby Sweden

SIGNATURE [Signature] DATE Aug 9, 2000

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IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: SANDSTROM, Torbjorn
Int'l. Appl. No.: PCT/SE99/00310
Appl. No.: New Group:
Filed: August 29, 2000 Examiner:
For: IMPROVED PATTERN GENERATOR

PRELIMINARY AMENDMENT

BOX PATENT APPLICATION

Assistant Commissioner for Patents
Washington, DC 20231

August 29, 2000

Sir:

The following Preliminary Amendments and Remarks are respectfully submitted in connection with the above-identified application.

AMENDMENTS

IN THE SPECIFICATION:

Please amend the specification as follows:

Before line 1, insert --This application is the national phase under 35 U.S.C. § 371 of PCT International Application No. PCT/SE99/00310 which has an International filing date of March 2, 1999, which designated the United States of America.--

IN THE CLAIMS:

Please amend the claims as follows:

Claim 7: Lines 1 to 2, change any of the claims 4-6" to --claim 4--

Claim 11: Lines 1 to 2, change "any of the preceding claims" to --claim 1--

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Claim 12: Lines 1 to 2, change "any of the previous claims"
to --claim 1--

Claim 17: Lines 1 to 2, change "any of the claims 1-16" to
--claim 1--

Claim 18: Lines 1 to 2, change "any of the claims 1-16" to
--claim 1--

Claim 19: Lines 1 to 2, change "any of the claims 1-16" to
--claim 1--

Claim 20: Lines 1 to 2, change "any of the claims 1-16" to
--claim 1--

Claim 21: Lines 1 to 2, change "any of the claims 1-16" to
--claim 1--

Claim 22: Lines 1 to 2, change "any of the claims 1-16" to
--claim 1--

Claim 23: Lines 1 to 2, change "any of the previous claims"
to --claim 1--

Claim 24: Lines 1 to 2, change "any of the claims 1-22" to
--claim 1--

Claim 25: Lines 1 to 2, change "any of the preceding
claims" to --claim 1--

Claim 27: Lines 1 to 2, change "any of the preceding
claims" to --claim 1--

Claim 29: Lines 1 to 2, change "any of the preceding
claims" to --claim 1--

Claim 30: Lines 1 to 2, change "any of the preceding
claims" to --claim 1--

Claim 32: Lines 1 to 2, change "any of the claims 1-29" to --claim 1--

Claim 33: Lines 1 to 2, change "any of the preceding claims" to --claim 1--

Claim 34: Line 1, delete "or 26"

Claim 35: Lines 1 to 2, change "any of the preceding claims" to --claim 1--

Claim 36: Lines 1 to 2, change "any of the preceding claims" to --claim 1--

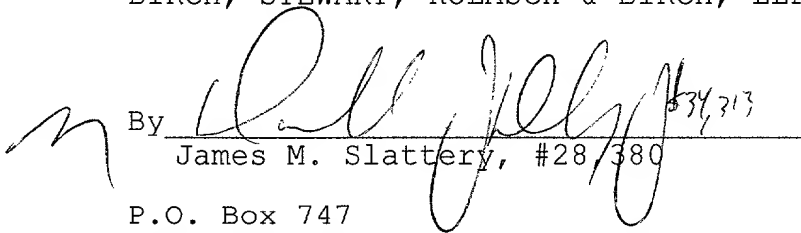
REMARKS

The specification has been amended to provide a cross-reference to the previously filed International Application. The claims have also been amended to delete multiple dependents and to place the application into better form for examination. Entry of the present amendment and favorable action on the above-identified application are respectfully requested.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 
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IMPROVED PATTERN GENERATORField of the invention

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The present invention relates to printing of patterns with extremely high precision on photosensitive surfaces, such as photomasks for semiconductor devices and displays. It also relates to direct writing of semiconductor device patterns, display panels, integrated optical devices and electronic interconnect structures. Furthermore, it can have applications to other types of precision printing such as security printing. The term printing should be understood in a broad sense, meaning exposure of photoresist and photographic emulsion, but also the action of light on other light sensitive media such as dry-process paper, by ablation or chemical processes activated by light or heat. Light is not limited to mean visible light, but a wide range of wavelengths from infrared (IR) to extreme UV. Of special importance is the ultraviolet range from 370 nm (UV) through deep ultraviolet (DUV), vacuum ultraviolet (VUV) and extreme ultraviolet (EUV) down to a few nanometers wavelength. EUV is in this application defined as the range from 100 nm and down as far as the radiation is possible to treat as light. A typical wavelength for EUV is 13 nm. IR is defined as 780 nm up to about 20 μ m.

In a different sense the invention relates to the art and science of spatial light modulators and projection displays and printers using such modulators. In particular it improves the grey-scale properties, the image stability through focus and image uniformity and the data processing for such modulators by application of analog modulation technique. The most important use of the analog modulation is to generate an image in a high-contrast material such as photoresist with an address

grid, i.e. the increment by which the position of an edge in the pattern is specified, that is much finer than the grid created by the pixels of the spatial light modulator.

5

Background of the invention

It is known in the current art to build precision pattern generators using projection of micromirror spatial light modulators (SLMs) of the micromirror type (Nelson 1988, Kück 1990). To use an SLM in a pattern generator has a number of advantages compared to the more wide-spread method of using scanning laser spots: the SLM is a massively parallel device and the number of pixels that can be written per second is extremely high. The optical system is also simpler in the sense that the illumination of the SLM is non-critical, while in a laser scanner the entire beam path has to be built with high precision. Compared to some types of scanners, in particular electrooptic and acoustooptic ones, the micromirror SLM can be used at shorter wavelengths since it is a purely reflective device.

In both references cited above the spatial modulator uses only on-off modulation at each pixel. The input data is converted to a pixel map with one bit depth, i.e. with the values 0 and 1 in each pixel. The conversion can be done effectively using graphic processors or custom logic with area fill instructions.

In a previous application by the same inventor Sandström (Sandström et. al. 1990), the ability to use an intermediate exposure value at the boundary of a pattern element to fine-adjust the position of the element's edge in the image created by a laser scanner was described.

It is also known in the art to create a grey-scale image, preferably for projection display of video images and for printing, with an SLM by variation of the time a pixel is turned on or by printing the same pixel several times with the pixel turned on a varying number of times.

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The present invention devices a system for direct grey-scale generation with a spatial light modulator, with a special view to the generation of ultra-precision patterns. Important aspects in the preferred embodiments, are uniformity of the image from pixel to pixel and independence of exact placement of a feature relative to the pixels of the SLM and stability when focus is changed, either with intention or inadvertently.

10 Summary of the invention

It is therefore an object of the present invention to provide an improved pattern generator for printing of precision patterns.

This object is achieved with an apparatus according to the appended claims, providing an analog modulation of the pixels in the SLM.

Brief description of the drawings

Figure 1 shows a printer in prior art. The SLM consists of micromirrors that deflect the light from the lens pupil.

Figure 2 shows a number of pixel designs with the upper four pixels in an off state and the remaining five pixels turned on.

Figure 3 shows an array of pixels moving up and down like pistons, thereby creating a phase difference. This is how an edge can be fine-positioned with a phase-type SLM.

Figure 4 shows a schematic comparison between an SLM with deflecting mirrors and an SLM with deforming mirrors.

Figure 5 shows a flow diagram of a method for translating and feeding data to the SLM.

Figure 6 shows a preferred embodiment of a pattern generator according to the invention.

Figure 7 shows schematically possible complex amplitudes for different types of SLM.

Figure 8 shows schematically different types of controlling of the SLM mirrors.

Description of the invention

5 The basis for understanding the invention is the generic arrangement in Figure 1 which shows a generic projection printer with an SLM. Spatial light modulators based on reflection come in two varieties, the deflection type (Nelson) and the phase type (Kück). The difference
10 between them may in a particular case with micromirrors seem small, but the phase SLM extinguishes the beam in the specular direction by destructive interference, while a pixel in a deflection SLM deflects the specular beam geometrically to one side so that it misses the aperture
15 of the imaging lens as shown in figure 1. For ultra-precise printing as performed in the current invention the phase-modulating system as described by Kück 1990 is superior to the deflecting type. First, it has better contrast since all parts of the surface, also hinges and
20 support posts, take part in the destructive interference and total extinction can be achieved. Second, a system that works by deflecting the light to the side is difficult to make symmetrical around the optical axis at intermediate deflection angles, creating a risk of
25 feature instability when focus is changed. In the preferred embodiments the phase type is used, but if one accepts or designs around the asymmetry of the deflection type it could also be used. This is illustrated schematically in fig 4. In the first figure, 4a, a non-
30 deflected micromirror 401 is illuminated, and the reflected light is not directed towards the aperture 402, and, hence, the light does not reach the substrate 403. In fig 4b, on the other hand, the mirror is fully deflected, and all the reflected light are directed
35 towards the apperture. In an intermediate position only part of the reflected light will reach the substrate, which is shown in fig 4c. However, in this case the light

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is not symmetrical around the optical axis for the lens 404, and there is an oblique incidence on the substrate. Hereby the distance between the lens and the substrate area becomes very critical, and small changes, such as

5 the one being indicated by the dashed position for area, causes significant displacements of the features on the substrate. A way to solve this problem is indicated by the fig 4d-f. Here, a first exposure is made with a first deflection angle for the micromirror, and thereafter a

10 second exposure, preferably with the same light dose, is made for a second deflection angle, being complementary to the first angle. Hereby, the combination of the first and second exposure is symmetrical around the optical axis for the lens. Another way to solve the problem is to

15 use deforming mirror 401', such as is shown in fig 4g, whereby the reflected light is evenly distributed over aperture. This last figure could schematically represent two cases, a phase type SLM (described below) or a deflection SLM, where light is reflected from different

20 parts of the mirror.

The phase SLM can be built either with micromachined mirrors, so called micromirrors, or with a continuous mirror surface on a supporting substrate that is possible to deform using an electronic signal. In Kück 1990 a

25 viscoelastic layer controlled by an electrostatic field is used, but it is equally possible, especially for very short wavelengths where deformations of the order of a few nanometer are sufficient, to use a piezoelectric solid disk that is deformed by electric field or another

30 electrically, magnetically or thermally controlled reflecting surface. For the remainder of this application an electrostatically controlled micromirror matrix (one- or two-dimensional) is assumed, although other arrangements as described above are possible, such as

35 transmissive or reflecting SLMs relying on LCD crystals or electrooptical materials as their modulation

mechanism, or micromechanical SLMs using piezoelectric or electrostrictive actuation.

The invention preferably uses a micromirror where the phase modulation is variable to obtain a variable amount of light reaching the pupil of the projection lens. Figure 2 shows some multi-element mirrors. The tilts of the various parts of the mirrors are unimportant. In fact one element by itself would direct the light toward the lens while another would direct it outside of the pupil. The correct way to understand the function is to look at the complex amplitude reaching the center of the pupil from each infinitesimal area element of the mirror and integrate the amplitude over the mirror. With a suitable shape of the mirror it is possible to find a deformation where the complex amplitudes add up to almost zero, corresponding to no light reaching the pupil. This is the off-state of the micromirror, while a relaxed state where the mirror surface is flat and the complex amplitudes add in phase is the on-state. Between the on and off-states the amount of light in the specular direction is a continuous but non-linear function of the deformation.

The pattern to be written is normally a binary pattern, such as a photomask pattern in chrome on a glass substrate. In this context binary means that there are no intermediate areas: a certain point on the photomask surface is either dark (covered with chrome) or clear (no chrome). The pattern is exposed in photoresist by the projected image from the SLM and the photoresist is developed. Modern resists have high contrast, meaning that a small percentage change in exposure makes the difference between full removal of the resist in the developer and hardly any removal at all. Therefore the photoresist has an edge that is normally almost perpendicular to the substrate surface, even though the aerial image has a gradual transition between light and dark. The chrome etching does further increase the

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contrast, so that the resulting image is perfectly binary: either opaque or clear with no intermediate areas.

The input data is in a digital format describing the geometry of the pattern to be written on the surface. The input data is often given in a very small address unit, e.g. 1 nanometer, while setting pixels in the SLM to either on or off gives a much coarser pattern. If a pixel on the SLM is projected to a $0.1\ \mu\text{m}$ pixel in the image, a line can only have a width that is an integer number of pixels ($n * 0.1\ \mu\text{m}$ where n is an integer). An address grid of $0.1\ \mu\text{m}$ was enough until recently, but the advent of so called optical proximity correction OPC makes a grid of 1 - 5 nanometers desirable. In OPC the size of features in the mask are modified slightly to compensate for predicted optical image errors when the mask is used. As an example, when a mask with four parallel lines $0.8\ \mu\text{m}$ wide is printed in a modern 4X reduction stepper (a projection printer for semiconductor wafers) they will in a typical case print as lines 0.187, 0.200, 0.200, and $0.187\ \mu\text{m}$ wide although they were intended to have the same width. This can be predicted by simulation of the image formation and the user of the mask may use OPC to compensate in the mask. therefore he wants the first and last line in the mask to be $4 * 0.213 = 0.852\ \mu\text{m}$ instead of $0.800\ \mu\text{m}$. With an address grid of $0.1\ \mu\text{m}$ he cannot make the correction, but with 5 nm address grid or finer such correction are possible.

In figure 5 the method for providing data for the SLM is shown in a flow diagram. The first step, S1, is to divide the pattern data for the pattern to be written into separate pattern fields. The pattern data is preferably received in digital form. Thereafter, in step S2, the fields are rasterised, and thereby assigned different exposure values. These values are then corrected for nonlinear response, step S3, and pixel-to-

pixel variations, step S4. Finally, the pixel values are converted to drive signals and forwarded to the SLM.

The invention preferably uses intermediate values between the off-state and on-state to create a fine address grid, e.g. 1/15, 1/25, 1/50, of the size of a pixel. A printed feature consists of pixels in the on state, but along the edge it has pixels set to intermediate values. This is done by driving the pixels with other voltages than the on and off voltages. Since there are several cascaded non-linear effects (the position of the edge versus exposure at the pixels at the boundary, the exposure vs. the deformation, and the deformation vs. the electric field) a non-linear transformation from the input data to the electric field is needed. Furthermore this transformation is calibrated empirically at regular time intervals.

Figure 3 shows an array of pixels moving up and down like pistons, thereby creating a phase difference. The figure shows how the pixels are controlled to create the reflectivity in the inset. The bright areas have pixels with 0 phase, while dark areas are created by pixels with alternating +90 and -90 degree phase. The oblique boundaries between bright and dark areas are created by intermediate values of phase. This is how an edge can be fine-positioned with a phase-type SLM. However, other types of SLM with intermediate values could be used in the same manner. The imaging properties with the phase SLM driven into intermediate values are complex and it is far from obvious that the edge will be moved in figure 3. However, it has been shown by extensive theoretical calculations and experiments by the inventor, that the described effect is real.

To create a finer address grid the electronic processing system is adapted to create one type of pixel map inside pattern features, another type of pixel map outside features, and intermediate pixel maps at a boundary, as shown in Figure 3, where the intermediate

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pixel map at a boundary is generated in dependence of the placement of the boundary in a grid finer than that of the pixels of the SLM projected on the workpiece. The SLM and the projection system creates one exposure level
5 inside features, another exposure level between features, and an intermediate exposure level at the boundary. The intermediate exposure level is created using the capability of the SLM to modulate to multiple states. The response from driving signals to actual placement of
10 the boundary is characterized and corrected for. It is empirically measured and a calibrating function is computed and stored in the data processing and delivery system.

To further improve the address resolution the stage
15 and the SLM are adapted to stitching exposure fields together along a direction not parallel to the coordinate system of the SLM, typically 45 degrees. In particular the continuous movement of the stage or optical system takes place in a direction not parallel to the SLM, but
20 typically at 45 degrees from the coordinate system of the SLM. It is also possible to have an SLM with non-orthogonal axes, in which case it is advantageous to have no axis parallel to the direction of motion. Furthermore, to suppress line errors resulting from
25 imperfections in the column and row drivers of the SLM of line error in the matrix itself it is effective to have the row and column lines at an angle to the direction of stitching, i.e. the vector between the centers of stitched fields.
30 Additional refinement of the address grid is created by the overlaying of at least two exposures with modified data such that the combined exposure has intermediate values not possible to obtain in a single exposure.

35 The design of the phase-type SLM

A cloverleaf mirror design, shown in fig 2c, as used in prior art is possible to drive to intermediate states

between on and off states. However, when the integrated complex amplitude is plotted as a function of deflection, it is seen that it never goes to zero completely but circles around zero, therefore having a non-zero minimum reflectivity with a varying phase angle. This is shown schematically by the line 701 in fig 7, where 703 indicates a position for a certain deformation value, and ϕ being the associated phase angle. A thorough analysis of an image with some pixels set to intermediate states shows that the position of the edges in the final image are not stable through focus if the integrated phase angle of the edge pixels is not zero. This is the diffraction effect analogous to the specular one shown in fig 4. In a preferred embodiment of the invention a new type of pixels with pivoting elements is used. Examples of such elements are shown in fig 2e-h. When the elements pivots one end moves toward the light source and the other end away from it thereby keeping the average phase close to zero. This is shown schematically by the dashed line 702 in fig 7. Furthermore the cloverleaf design has a problem of built-in stress created during the fabrication. This stress tends to give a partial deformation without an applied electric field. The built-in deformation is not perfectly the same in every pixel since it depends on imperfections during the manufacturing. In the cloverleaf design this difference from pixel to pixel creates a first-order variation of reflectivity. With pixel cells built from pivoting elements the same effect occurs, but gives a second-order effect. Therefore the uniformity is better in the projected image.

The design of the modulating elements and the exposure method are adapted to creating, for differently placed and/or differently oriented edges in the pattern, a symmetry in the aperture stop of the projection system. Inherent asymmetries between edges placed at different positions relative to the pixel grid can be reduced by

overlying at least two images with different placement of the pixel grid relative to the pattern.

For a deflection type of SLM the symmetry relates to the intensity distribution in the aperture stop. Best is to have modulating elements that deflect the light symmetrically relative to the center of the aperture stop, or else can overlayed exposures with complementary deflection be used to create symmetry. With modulating elements with steerable deflection it is possible to create a constant geometrical relation between the deflection at a pixel at an edge and the edge, i.e. directing it in a direction perpendicular to the edge and toward the interior of the feature.

With a diffraction type SLM it is possible to create symmetry by overlaying exposures with opposite phase maps. Symmetry can be maintained if the complex amplitude is everywhere real on the SLM, and pixels can be designed with the integrated complex amplitude being essentially real with values in the range -1 to 1. Many times it is sufficient with amplitudes in the range -0.5 to 1. This is the case with the square pivoting micromirror elements in Figure 2e,f,g,h.

With access to a small negative amplitude to print in the background areas it is possible for enhance the resolution. In a more complex scheme it is possible to drive groups of adjacent pixels to combine in the image, and after being filtered by the imaging system, to give a desired real amplitude.

In order to preserve symmetry it is beneficial to have at least 2-fold symmetry and preferably 4-fold. Symmetry can be created for pixels not having an inherent rotational symmetry by multiple overlayed exposures. Furthermore, with a pixel design or exposure sequence giving a controlled real amplitude can be used for resolution enhancements. Dark lines can be given extra contrast if placed between areas with opposite phase and the edge of a feature can be improved by driving adjacent

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pixels inside the feature to higher positive amplitudes or adjacent pixels outside to negative ones.

Image enhancements.

5 There is a third advantage with a pivoting design:
the cloverleaf does not reach full extinction, but a
pivoting cell can more easily be given a geometry that
gives full extinction, or even goes through zero and
comes back to a small non-zero reflection, but with
10 reversed phase. With better extinction there is greater
freedom to print overlapping exposures, designing for a
small negative value 702 gives better linearity close to
extinction. Printing with a weak exposure, approximately
5%, in the dark areas, but with reversed phase can give
15 an increased edge sharpness of 15 - 30 % and the ability
to print smaller features with a given lens. This is an
analog to so called attenuating phase-shifting masks that
are used in the semiconductor industry. A related method
of increasing the edge acuity is to set the pixels that
20 are inside a feature a lower value and those near the
edge a higher value. This gives a new type of image
enhancement not possible with current projection of
patterns from masks or by the use of projectors following
Nelson and Kück. The combination of a non-zero negative
25 amplitude in the background and an increased exposure
along the edges need not conflict with the creation of a
fine address grid by driving edge pixels to intermediate
values, since the effects are additive or at least
computable. When the pixels are substantially smaller
30 than the feature to be printed there exists a combination
of pixel values that creates all effects simultaneously.
To find them requires more computation than the
generation of a fine address grid alone, but in some
applications of the invention the ability to print
35 smaller features can have a high value that pays for the
extra effort.

In the case of a continuous mirror on a visc elastic layer there is an inherent balancing of the average phase to zero. Simulations have shown that the driving to intermediate values for fine positioning of feature edges work also for the continuous mirror. The non linearities are smaller than with micromirrors. But for the method to work well the minimum feature has to be larger than with micromirrors, i.e. have a larger number of addressed pixels per resolved feature element is needed.

Consequences are a larger SLM device and that for given pattern the amount of data is larger. Therefore the micromirrors have been chosen in a first and second embodiment.

In the invention a pixel with rotation-symmetrical deformation (at least two-fold symmetry, in a preferred embodiment four-fold symmetry) is used for two reasons: to give a symmetrical illumination of the pupil of the projection lens and to make the image insensitive to rotations. The latter is important for printing a random logic pattern on a semiconductor wafer. If there is an x-y asymmetry the transistors laid-out along the x axis will have a different delay from those along the y axis and the circuit may malfunction or can only be used at a lower clock-speed. The two requirements of image invariance through focus and symmetry between x and y makes it very important to create and maintain symmetries in the optical system. Symmetry can be either inherent or it can be created by deliberate balancing of assymmetric properties, such as using multiple exposures with complementary assymmetric properties. However, since multiple exposures lead to reduced through-put inherent symmetrical layouts are strongly favoured.

Preferred embodiments

A first preferred embodiment is a deep-UV pattern generator for photomasks using an SLM of 2048 x 512

micromirrors. The light source is an KrF excimer laser with a pulsed output at 248 nanometers, pulse lengths of approximately 10 ns and a repetition rate of 500 Hz. The SLM has an aluminum surface that reflects more than 90% of the light. The SLM is illuminated by the laser through a beam-scrambling illuminator and the reflected light is directed to the projection lens and further to the photosensitive surface. The incident beam from the illuminator and the exiting beam to the lens are separated by a semitransparent beamsplitter mirror. Preferably the mirror is polarisation-selective and the illuminator uses polarised light, the polarisation direction of which is switched by a quarter-wave plate in front of the SLM. For x and y symmetry at high NA the image must be symmetrically polarised and a second quarter-wave plate between the beamsplitter and the projection lens creates a circularly polarised image. A simpler arrangement when the laser pulse energy allows it is to use a non-polarising beamsplitter. The quarter-wave plate after the second pass through the beamsplitter is still advantageous, since it makes the design of the beam-splitting coating less sensitive. The simplest arrangement of all is to use an oblique incidence at the SLM so that the beams from the illuminator and to the projection lens are geometrically separated, as in figure 1.

The micromirror pixels are $20 \times 20 \mu\text{m}$ and the projection lens has a reduction of 200X, making one pixel on the SLM correspond to $0.1 \mu\text{m}$ in the image. The lens is a monochromatic DUV lens with an NA of 0.8, giving a point spread function of $0.17 \mu\text{m}$ FWHM. The minimum lines that can be written with good quality are $0.25 \mu\text{m}$.

The workpiece, e.g. a photomask, is moved with an interferometer-controlled stage under the lens and the interferometer logic signals to the laser to produce a flash. Since the flash is only 10 ns the movement of the stage is frozen during the exposure and an image of the

SLM is printed, $204.8 \times 51.2 \mu\text{m}$ large. 2 milliseconds later the stage has moved $51.2 \mu\text{m}$, a new flash is shot and a new image of the SLM is printed edge to edge with the first one. Between the exposures the data input system has loaded a new image into the SLM, so that a larger pattern is composed of the stitched flashes. When a full column has been written the stage advances in the perpendicular direction and a new row is started. Any size of pattern can be written in way, although the first preferred embodiment typically writes patterns that are $125 \times 125 \text{ mm}$. To write this size of pattern takes 50 minutes plus the time for movement between consecutive columns.

Each pixel can be controlled to 25 levels (plus zero) thereby interpolating the pixel of $0.1 \mu\text{m}$ into 25 increments of 4 nanometers each. The data conversion takes the geometrical specification of the pattern and translates it to a map with pixels set to on, off or intermediate reflection. The datapath must supply the SLM with $2048 * 512 * 500$ words of data per second, in practice 524 Mbytes of pixel data per second. In a preferred embodiment the writable area is maximally $230 \times 230 \text{ mm}$, giving up to $230 / 0.0512 = 4500$ flashes maximum in a column and the column is written in $4500 / 500 = 9$ seconds. The amount of pixel data needed in one column is $9 * 524 = 4800 \text{ Mb}$. To reduce the amount of transferred and buffered data a compressed format is used, similar to the one in Sandström et al. 90, but with the difference that a pixel map is compressed instead of segments with a length and a value. A viable alternative is to create a pixel map immediately and use commercially available hardware processors for compression and decompression to reduce the amount of data to be transferred and buffered.

Even with compression the amount of data in a full mask makes it highly impractical to store pre-fractured data on disk, but the pixel data has to be produced when it is used. An array of processors rasterise the image in

parallel into the compressed format and transfer the compressed data to an expander circuit feeding the SLM with pixel data. In the preferred embodiment the processors rasterise different parts of the image and
5 buffer the result before transmitting them to the input buffer of the expander circuit.

A second preferred embodiment

In a second preferred embodiment the laser is an ArF
10 excimer laser with 193 nm wavelength and 500 Hz pulse frequency. The SLM has 3072 x 1024 pixels of $20 * 20 \mu\text{m}$ and the lens has a reduction of 333X giving a projected pixel of $0.06 \mu\text{m}$. There are 60 intermediate values and the address grid is 1 nanometer. The point spread
15 function is $0.13 \mu\text{m}$ and the minimum line $0.2 \mu\text{m}$. The data flow is 1572 Mbytes/s and the data in one column 230 mm long is 11.8 Gb.

A third preferred embodiment is identical with the second one except that the matrix of pixels is rotated 45
20 degrees and the pixel grid is $84 \mu\text{m}$, giving a projected pixel spacing along x and y of $0.06 \mu\text{m}$. The laser is an ArF excimer laser and the lens the reduction of 240. Because of the rotated matrix the density of pixels in the matrix is less and the data volume is half of the
25 previous embodiment but with the same address resolution.

Laser flash to flash variations

The excimer laser has two unwanted properties, flash-to-flash energy variations of 5% and flash-to-flash
30 time jitter of 100 ns. In the preferred embodiments both are compensated in the same way. A first exposure is made of the entire pattern with 90% power. The actual flash energy and time position for each flash is recorded. A second exposure is made with nominally 10 % exposure and
35 with the analog modulation used to make the second exposure 5 - 15% depending on the actual value of the first one. Likewise a deliberate time offset in the

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second exposure can compensate for the time jitter of the first one. The second exposure can fully compensate the errors in the first, but will itself give new errors of the same type. Since it is only on average 10% of the total exposure both errors are effectively reduced by a factor of ten. In practice the laser has a time uncertainty that is much larger than 100 ns, since the light pulse comes after a delay from the trigger pulse and this delay varies by a couple of microseconds from one time to another. Within a short time span the delay is more stable. Therefore the delay is measured continuously and the last delay values, suitably filtered, are used to predict the next pulse delay and to position the trigger pulse.

It is possible to make corrections for stage imperfections in the same way, namely if the stage errors are recorded and the stage is driven with a compensating movement in the second exposure. Any placement errors that can be measured can in principle be corrected in this way, partially or fully. It is necessary to have a fast servo to drive the stage to the computed points during the second exposure. In prior art it is known to mount the SLM itself on a stage with small stroke and short response time and use it for fine positioning of the image. Another equally useful scheme is to use a mirror with piezoelectric control in the optical system between the SLM and the image surface, the choice between the two is made from practical considerations. It is also possible to add a position offset to the data in an exposure field, and thereby move the image laterally.

The second exposure is preferably done with an attenuating filter between the laser and the SLM so that the full dynamic range of the SLM can be used within the range 0 - 15 % of the nominal exposure. With 25 intermediate levels it is possible to adjust the exposure in steps of $15\% \cdot 1/25 = 0.6\%$.

The response varies slightly from pixel to pixel due to manufacturing imperfections and, potentially, also from ageing. The result is an unwanted inhomogeneity in the image. Where image requirements are very high it may be necessary to correct every pixel by multiplication with the pixels inverse responsivity which is stored in a lookup memory. Even better is the application of a polynomial with two, three or more terms for each pixel. This can be done in hardware in the logic that drives the SLM.

In a more complex preferred embodiment several corrections are combined into the second corrective exposure: the flash to flash variation, flash time jitter, and also the known differences in the response between the pixels. As long as the corrections are small, i.e. a few percent in each they will add approximately linearly, therefore the corrections can be simply added before they are applied to the SLM. The sum is multiplied with the value desired exposure dose in that pixel.

Alternative illumination sources

The excimer laser has a limited pulse repetition frequency (prf) of 500 - 1000 Hz depending on the wavelength and type of the laser. This gives large fields with stitching edges in both x and y. In two other preferred embodiments the SLM is illuminated with a pulsed laser with much higher prf, e.g. a Q-switched upconverted solid state laser, and with a continuous laser source scanned over the surface of the SLM, so that one part of the SLM is reloaded with new data while another part is printed. In both cases the coherence properties of the lasers are different from the excimer laser and a more extensive beam-scrambling and coherence control is needed, e.g. multiple parallel light paths with different pathlengths. In some implementations of the invention the light output from a flash lamp is

sufficient and can be used as the light source.
Advantages are low cost and good coherence properties.

In the preferred embodiment with scanning illumination two issues are resolved: the pulse to pulse
5 variation in time and energy, since the scanning is done under full control preferably using an electrooptic scanner such as acoustooptic or electrooptic, and many continuous laser have less power fluctuation than pulsed lasers. Furthermore the use of continuous lasers gives a
10 different selection of wavelengths and continuous lasers are less dangerous to the eye than pulsed lasers. Most important, however, is the possibility of reaching much higher data rates with a matrix with only a few lines since the scanning is non-critical and can be done with
15 100 kHz repetition rate or higher. Scanning the illumination beam is also a way of creating a very uniform illumination, which is otherwise difficult.

In some embodiments it is possible, and feasible, to use a flash lamp as the illumination source.

20

EUV

Light sources for EUV are based on radiation from a particle accelerator, a magnetic plasma pinch machine or the heating of a small drop of matter to extreme
25 temperatures with a high-power laser pulse. In either case the radiation is pulsed. The EUV radiation propagates only in vacuum and can only be focused by reflective optics. A typical pattern generator using an SLM has a small exposure field a modest requirement of
30 optical power. The design of the optical system is therefore relaxed compared to that of an EUV stepper, making it possible to use more mirrors and go to higher NA than in a stepper. It is anticipated that a high-NA lens will have a ring-shaped exposure field and it is
35 fully possible to adapt the shape of the SLM to such a field. With a wavelength of 13 nm and an NA of 0.25 it is possible to expose lines that are only 25 nm wide, and,

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using image enhancement as described below, even below 20 nm. No other known writing technology can match this resolution and at the same time the writing speed that is made possible by the parallel character of an SLM.

5

Edge overlap

Since a two-dimensional field is printed for each flash and the fields are stitched together edge to edge to edge the stitching is very critical. A displacement of only a few nanometers of one field will create pattern errors along that edge that are visible and potentially detrimental to the function of an electronic circuit produced from the mask. An effective way of reducing the unwanted stitching effects is to print the same pattern in several passes but with a displacement of the stitching boundaries between the passes. If the pattern is printed four times the stitching error will occur in four positions, but with only a fourth of the magnitude. In a preferred embodiment of the current invention the ability to create intermediate exposures is used together with an overlap band between the fields. The values are computed during the rasterisation, although it could also be done during the expansion of the compressed data. Edge overlap reduces the stitching errors with much less throughput penalty than multipass printing.

Modified illumination

In the first preferred embodiment the illumination of the SLM is done by an excimer laser and a light scrambler such as a fly-eye lens array to create an illumination that resembles that from a circular self-luminous surface in the pupil plane of the illuminator. In order to increase the resolution when printing with a specific projection system it is possible to use a modified illumination. In the most simple cases pupil filters are introduced in the pupil plane of the illuminator, e.g. with a quadrupole-shaped or annular

transmission area. In a more complex case the same field is printed several times. Several parameters can be made to vary between the exposures, such as focus in the image plane, illumination pattern, data applied to the SLM and pupil filter in the pupil plane of the projection optics. In particular the synchronised variation of the illumination and a pupil filter can give an increased resolution, most notably if the pupil has and a sector-shaped transmitting area and the illumination is aligned so that the non-diffracted light intercepts an absorbing patch near the apex of the sector

Linearisation of the response

For linearisation of the transfer function from data to edge placement there are essentially three ways to go:

- taking the non-linearity into account in the data conversion unit and generating an 8 bit (example) pixel values in the data conversion unit and use DACs with the same resolution to drive the SLM. This is shown schematically in fig 8a, where R are relay signals and C are capacitors being provided on each matrix element on the SLM. The SLM is indicated by the dashed line.
- to generate digital values with fewer values, e.g. 5 bits or up to 32 values, and translate them to an 8 bit value in a look-up table (LUT) and then feed the 8 bit values to the DACs.
- to use a 5 bit value and semiconductor switches to select a DC voltage that is generated by one or several high-resolution DACs. This is shown schematically in fig 8b.

In either case it is possible to measure an empirical calibration function such that the response on the plate is linearised, when said function being applied at respectively the data conversion unit, the LUT or in the DC voltages.

Which linearization scheme to use depends on the data rate, the precision requirements and also on the available circuit technology that may change over time. At the present time the data conversion unit is a
5 bottleneck and therefore it is not a preferred solution to linearise in the data conversion unit, neither to generate 8-bit pixel values. High-speed DACs are expensive and power-hungry and the most appropriate solution is to generate DC voltages and use switches. It
10 is then possible to use even higher resolution than 8 bits.

Description of a preferred pattern generator

Referring to figure 6, a pattern generator comprises
15 an SLM 601 with individual and multi-value pixel addressing, an illumination source 602, an illumination beam scrambling device 603, an imaging optical system 604, a fine positioning substrate stage 605 with an interferometer position control system 606 and a hardware
20 and software data handling system 607 for the SLM. For proper functionality and ease of operation it also contains a surrounding climate chamber with temperature control, a substrate loading system, software for timing of stage movement and exposure laser triggering to
25 achieve optimum pattern placement accuracy and a software user interface.

The illumination in the pattern generator is done with a KrF excimer laser giving a 10-20 nanoseconds long light flash in the UV region at 248 nanometer wavelength
30 with a bandwidth corresponding to the natural linewidth of an excimer laser. In order to avoid pattern distortion on the substrate, the light from the excimer laser is uniformly distributed over the SLM surface and the light has a short enough coherence length not to produce laser
35 speckle on the substrate. A beam scrambler is used to achieve these two aims. It divides the beam from the excimer laser in several beam paths with different path

length and then adds them together in order to reduce the spatial coherence length. The beam scrambler also has a beam homogenizer consisting of a lens system containing a set of fly-eye lenses that distributes the light from each point in the laser beam from the excimer laser uniformly over the SLM surface giving a "top-hat" light distribution. This beam scrambling, homogenizing and reduction of coherence is advantageous in all SLM printers. Depending on the actual circumstances implementations using beam-splitters and combiners, diffractive elements, optical fibres, kaleidoscopes, lenslet arrays, prisms or prism arrays or integrating spheres can be used, as well as other similar devices in combinations which split and combines the beams to create a multitude of mutually incoherent light fields impinging on the SLM.

The light from the SLM is relayed and imaged down to the substrate on the substrate stage. This is done using a Schlieren optical system described by Kück. A lens l_1 with the focal width f_1 is placed at the distance f_1 from the SLM. Another lens l_2 with the focal length f_2 is placed at the distance $2 \times f_1 + f_2$ from the SLM. The substrate is then at a distance $2 \times f_1 + 2 \times f_2$ from the SLM. At the distance $2 \times f_1$ from the SLM there is an aperture 608 which size determines the numerical aperture (NA) of the system and thereby the minimum pattern feature size that can be written on the substrate. In order to correct for imperfections in the optical system and the substrate flatness there is also a focal system that dynamically positions the lens l_2 in the z direction with a position span of 50 micrometers to achieve optimal focal properties. The lens system is also wavelength corrected for the illuminating wavelength of 248 nanometers and has a bandwidth tolerance of the illuminating light of at least ± 1 nanometer. The illuminating light is reflected into the imaging optical system using a beamsplitter 609 that is positioned

immediately above the lens l_1 . For a demagnification factor of 250 and an NA of 0.62 it is possible to expose pattern features with a size down to 0.2 micrometers with a good pattern quality. With 32 levels from each SLM-pixel the minimum gridsize is 2 nanometers.

The pattern generator has a fine positioning substrate stage with an interferometer position control system. It consists of a moveable air bearing xy table 605 made of zerodur for minimum thermal expansion. A servo system with an interferometer position feedback measuring system 606 controls the stage positioning in each direction. In one direction, y, the servo system keeps the stage in fixed position and in the other direction, x, the stage moves with continuous speed. The interferometer position measuring system is used in the x-direction to trigger the exposure laser flashes to give uniform position between each image of the SLM on the substrate. When a full row of SLM images are exposed on the substrate the stage moves back to the original position in the x direction and moves one SLM image increment in the y direction to expose another row of SLM images on the substrate. This procedure is repeated until the entire substrate is exposed.

The SLM images overlaps with a number of pixels in both the x and y direction and the exposure data pattern is locally modified in the overlap pixels to compensate for the increased number of exposures that result in such overlap areas.

Variations in pulse to pulse intensity from the excimer laser is compensated by using two-pass exposure of the pattern where the first pass is done using a nominal 90% intensity of the correct intensity. In the first pass, the actual intensity in each laser flash is measured and stored. In the second pass, the correct intensity for each SLM image exposure is then used based on the measured intensity values from the first pass. In this way it is possible to reduce the influence from

610. Since the grayscale steps of an exposed pixel is not linear in response to the voltage applied to the pixel electrode, the input data is linearized in a pixel linearizer 611 so that the 32 gray levels correspond to a uniform increase in exposure dose for each successive level. This is done using 8-bit digital to analog converters (DAC) 612 where each gray level from the pixel map selects a voltage from the DAC's according to a previously empirically calibrated linearization function. An additional offset in the choice of analog level from the DAC's is made using a lookup table where each value corresponds to an SLM pixel and each such value corrects for anomalies of the corresponding pixel. The calibration values in the lookup table are generated using an empirical calibration procedure where a series of test patterns are sent to the SLM and the resulting exposed patterns are measured and used for individual pixel correction. This means that each gray level in the pixel map selects an analog voltage generating a pixel deformation for every corresponding SLM pixel to give the correct exposure dose.

References

- 25 Nelson 1988: US patent US 5,148,157
Kück 1990: European patent EP 0 610 183
Sandström et al. 1990: European patent EP 0 467 076

CLAIMS

1. An apparatus for creating a pattern on a workpiece sensitive to light radiation, such as a photomask a display panel or a microoptical device,
5 comprising
a source for emitting light in the wavelength range from EUV to IR,
a spatial light modulator (SLM) having a multitude of modulating elements (pixels), adapted to being
10 illuminated by said radiation
a projection system creating an image of the modulator on the workpiece,
an electronic data processing and delivery system receiving a digital description of the pattern to be
15 written, converting said pattern to modulator signals, and feeding said signals to the modulator,
a precision mechanical system for positioning said workpiece and/or projection system relative to each other
an electronic control system controlling the
20 position of the workpiece, the feeding of the signals to the modulator and the intensity of the radiation, so that said pattern is printed on the workpiece
where the drive signals and the modulating elements are adapted to create a number of modulation states
25 larger than two and preferably larger than three.
2. An apparatus according to claim 1, where a modulating element modulates at least one of the following properties of the radiation:
- intensity
 - 30 • phase
 - complex amplitude
 - direction
 - polarisation
 - wavefront flatness
 - 35 • frequency

where the modulation impressed on the radiation field has at least three, and preferably at least four, different states.

3. An apparatus according to claim 2, having a
5 filter selective to the state of the radiation, thereby translating the modulation across the surface of the spatial modulator to an intensity image at the workpiece.

4. An apparatus according to claim 1, where a
compensating linearisation function is used in the
10 conversion from the input pattern description to the modulator voltages to correct for a non-linear response from the modulator voltages to the exposure on the workpiece.

5. An apparatus according to claim 4, where the
15 linearization function is based on a theoretical simulation.

6. An apparatus according to claim 4, where the linearization function is based on an empirical characterisation of the response.

7. An apparatus according to any of the claims
20 4-6, where said response is the physical or chemical result of the exposure of a surface element, such as the light absorption of a developed silver halide emulsion, or the removal of material mass per surface area by an
25 ablation process.

8. An apparatus according to claim 4, where the
desired responses are computed as digital values and the linearisation function is stored in a look-up table
generating new corrected digital values used for
30 generation of the modulator drive voltages.

9. An apparatus according to claim 8, where the modulator voltages are created by digital analog converters.

10. An apparatus according to claim 4, where the
35 desired responses are computed as digital values and the values are used to select for each modulating element one

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of several independently generated voltages, and where said voltages are set to contain the linearisation function.

11. An apparatus according to any of the preceding claims, where at least two modulator voltage signals are fed to a single modulator element and the modulator element is responsive to the combination of the signals, thereby being driven to a larger number of states than the number of possible voltage values in each signal, e.g. four binary signals creating sixteen different states of the modulating element.

12. An apparatus according to any of the previous claims, with a look-up table with corrections for different response between modulator elements.

13. An apparatus according to claim 12, where the look-up table is generated during a calibration procedure where the response function of at least two different modulator elements are measured.

14. An apparatus according to claim 12, where said look-up table storing at least one of the following types of data for a modulating element:

- An offset voltage
- A sensitivity factor
- A polynomial response function.

15. An apparatus according to claim 12, where the correction of a modulating element is applied as a digital operation on a digital representation of the desired state of the modulator element.

16. An apparatus according to claim 12, where the correction of a modulating element is applied by an analog operation on the analog drive signal.

17. An apparatus according to any of the claims 1-16, where the spatial modulator is a two-dimensional array of modulating elements with time-multiplexed loading of the values to the modulating elements and storage of the loaded value at each element.

18. An apparatus according to any of the claims 1-16, where the modulator is built on a matrix-addressed active circuit

19. An apparatus according to any of the claims 1-16, where the modulator is built on top of a semiconductor chip.

20. An apparatus according to any of the claims 1-16, where the modulator contains a liquid crystal.

21. An apparatus according to any of the claims 1-16, where the modulator has a viscoelastic layer.

22. An apparatus according to any of the claims 1-16, where the modulator has an array of micromechanical elements, and preferably an array of micromirrors, and most preferably an array of pyramidal micromirrors.

23. An apparatus according to any of the preceding claims, where the modulator is reflective.

24. An apparatus according to any of the claims 1-22, where the modulator is transmissive.

25. An apparatus according to any of the preceding claims, where the input pattern is decomposed into a number of exposure fields, and said exposure fields are exposed at different positions on the workpiece, thereby stitching together the complete pattern from said exposure fields.

26. An apparatus according to claim 25, where the stage and projection system are adapted to making strokes of continuous travel relative to each other, and the electronic control system coordinates the motion, the loading of the modulator drive signals and the illumination, in such a way that at least two, and preferably at least ten, exposure fields are exposed during an uninterrupted stroke.

27. An apparatus according to any of the preceding claims, further comprising a timing unit controlling the timing of the emission of radiation from the light source.

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28. An apparatus according to claim 27, whereby the timing unit predicts the time delay for a control signal to the light source emission according to the previous measurements, and compensates for the same.

5 29. An apparatus according to any of the preceding claims, whereby the light source is a laser, and preferably an excimer laser.

30. An apparatus according to any of the preceding claims, where the illumination of at least one modulating
10 element of the SLM is pulsed, and preferably the illumination of the entire SLM.

31. An apparatus according to claim 30, where the pulse length (full width half maximum or equivalent) of the illumination of a modulating element of the SLM is
15 shorter than the time to travel a distance corresponding to three pixels projected on the workpiece.

32. An apparatus according to any of the claims 1-29, where the illumination of at least one modulating element of the SLM is continuous and scanned, and
20 preferably the illumination of the entire SLM.

33. An apparatus according to any of the preceding claims, where the digital description of the pattern is in a symbolic format, e.g. a vector or algorithmic format.

25 34. An apparatus according to claim 25 or 26, where the stage and/or optical system is adapted to producing a microlithographic pattern with features smaller than 30 μm and/or placement and size accuracy better than 3 μm (3 sigma).

30 35. An apparatus according to any of the preceding claims, where the pattern is formed in photoresist, photopolymer or photographic emulsion.

36. An apparatus according to any of the preceding claims, where the pattern is formed by ablation, a
35 photorefractive effect, a photochemical alteration of a component of the workpiece or by a thermal process.

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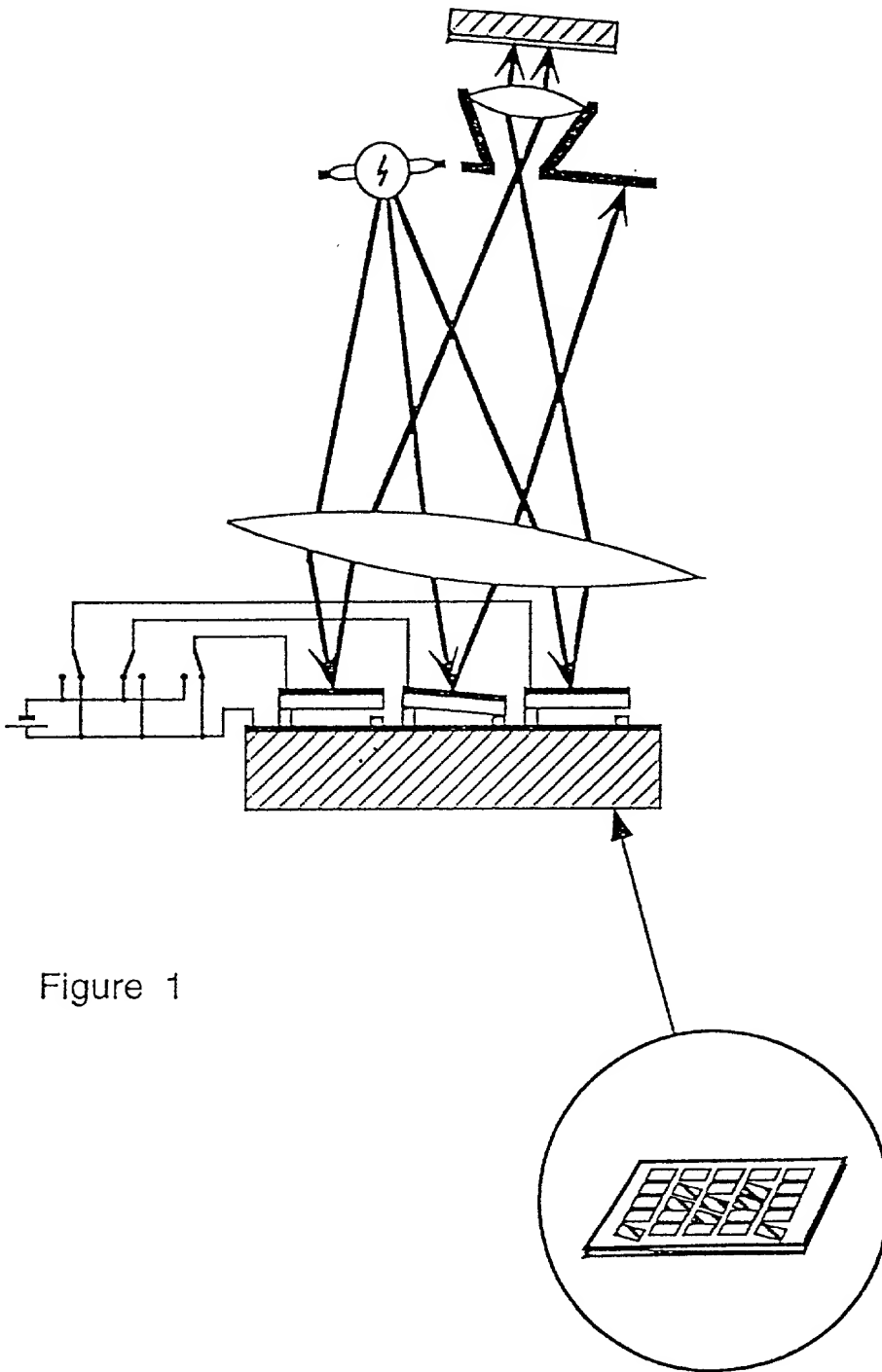


Figure 1

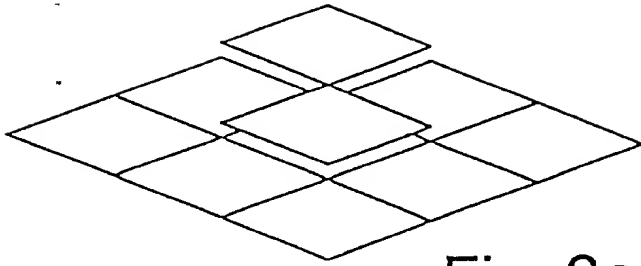


Fig. 2a

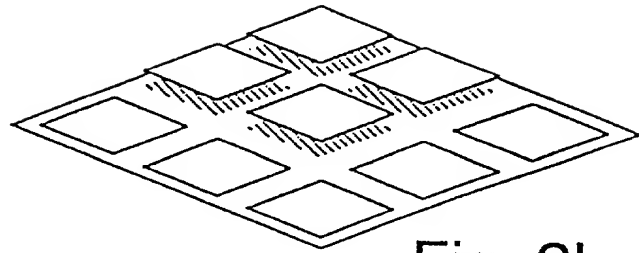


Fig. 2b

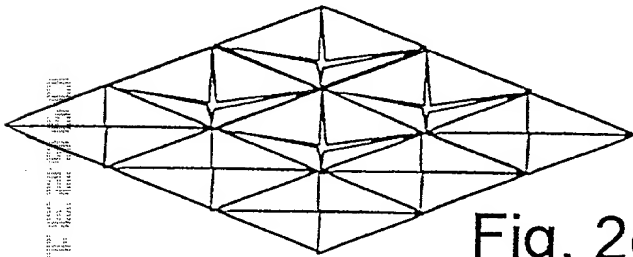


Fig. 2c

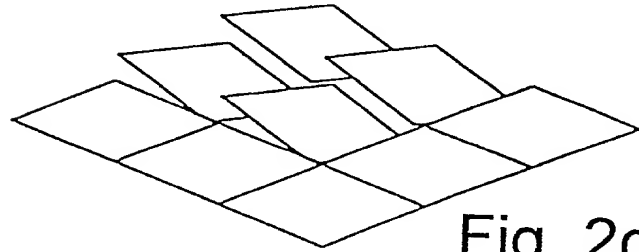


Fig. 2d

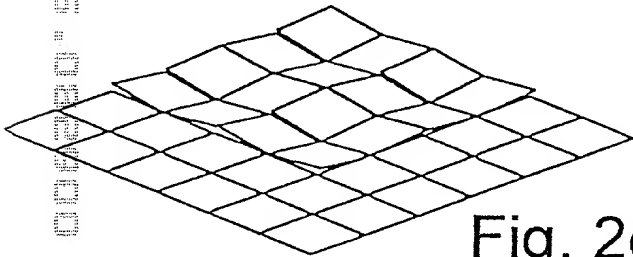


Fig. 2e

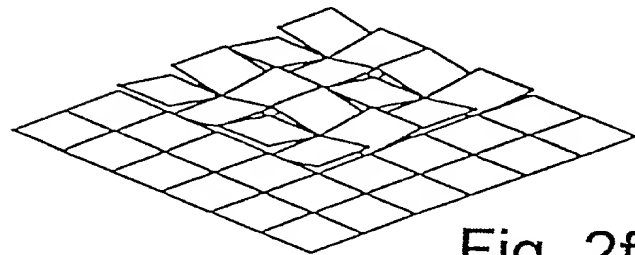


Fig. 2f

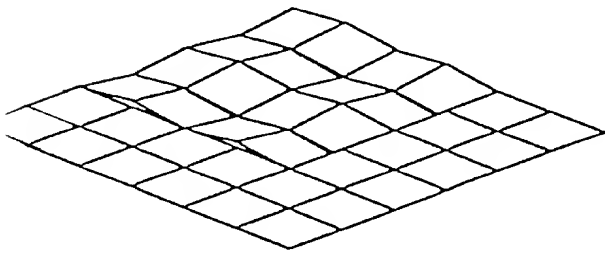


Fig. 2g

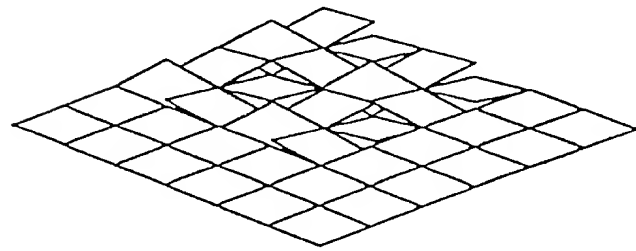


Fig. 2h

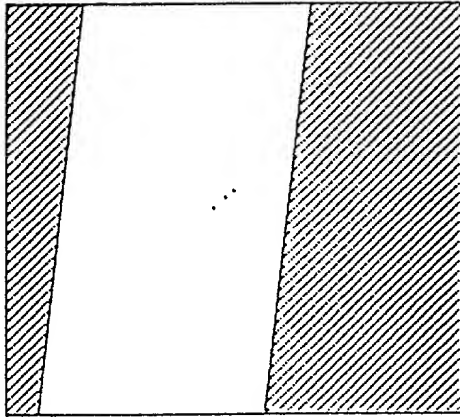
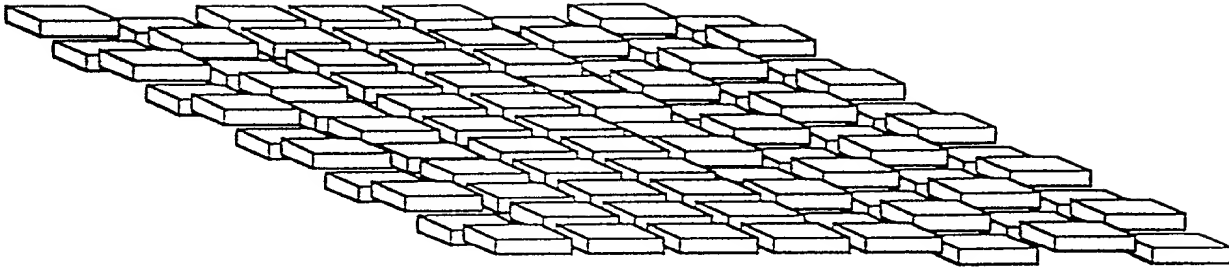


Fig 3

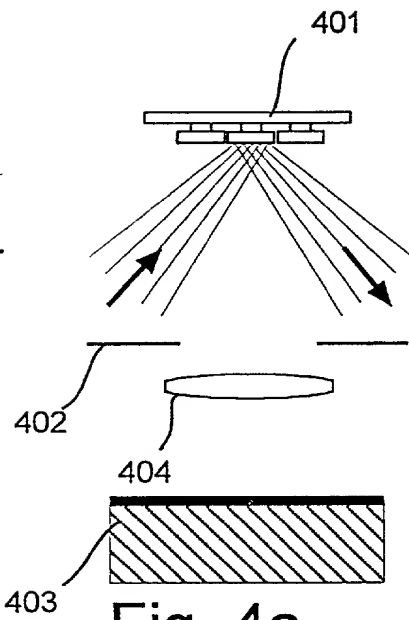


Fig. 4a

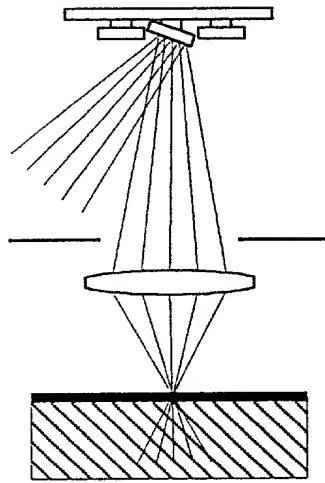


Fig. 4b

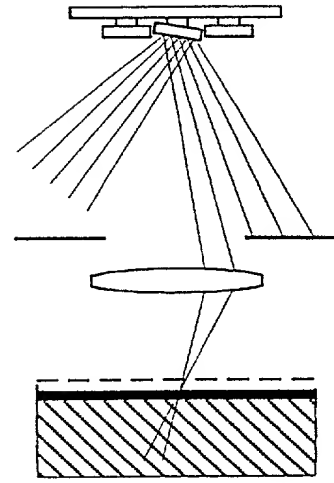


Fig. 4c

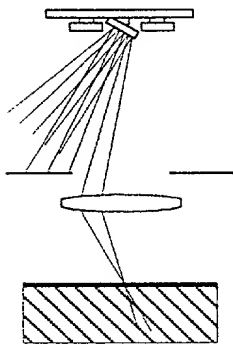


Fig. 4d

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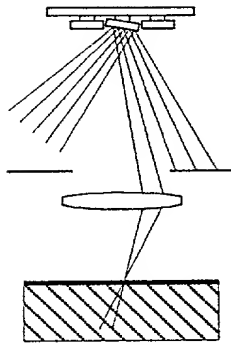


Fig. 4e

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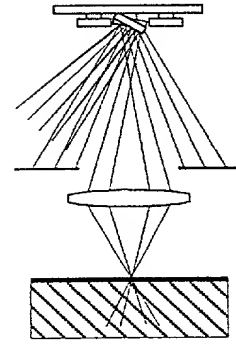


Fig. 4f

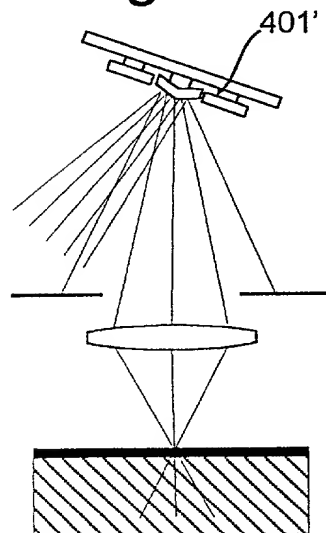


Fig. 4g

Fig. 4

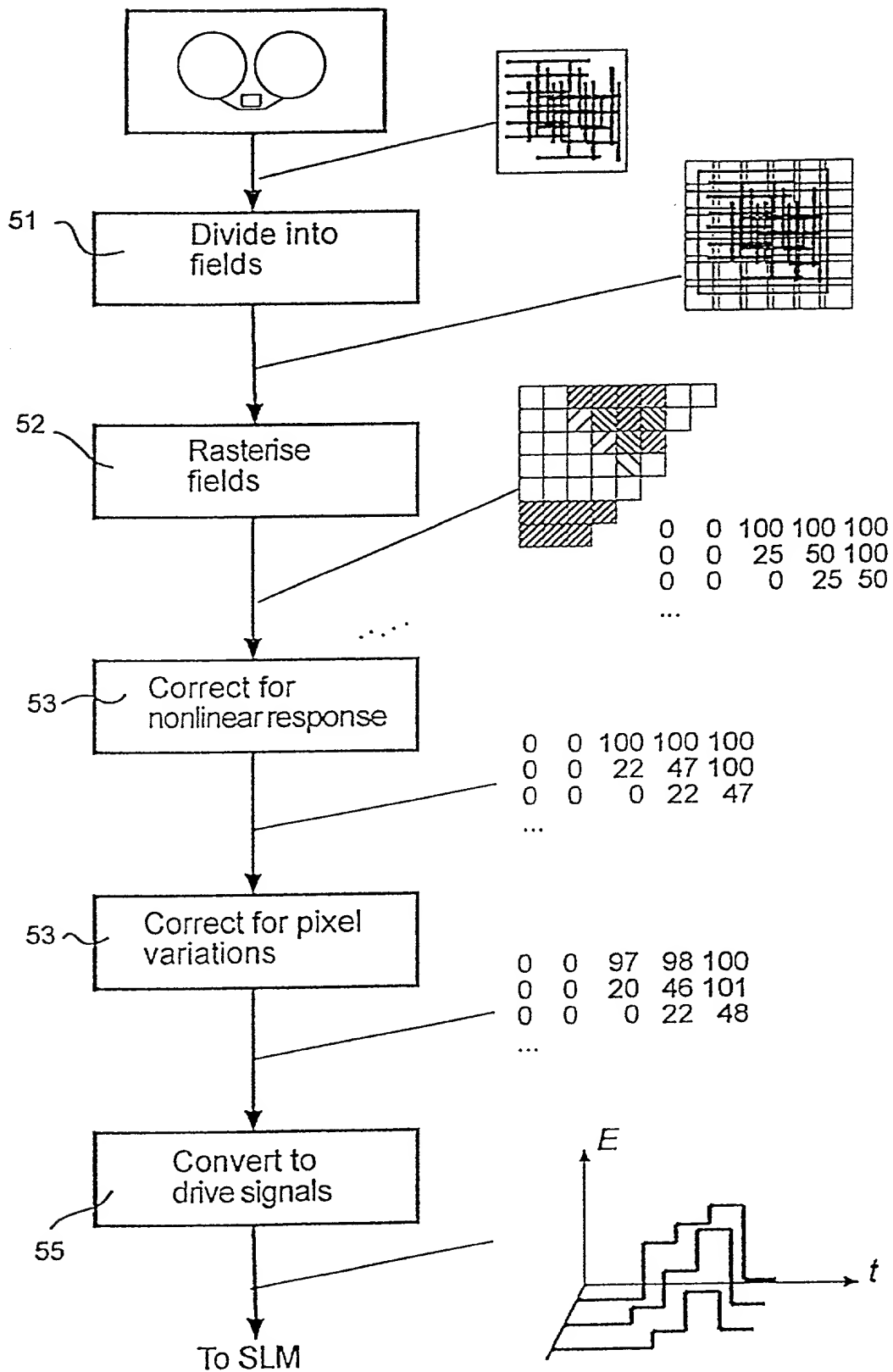


Fig. 5

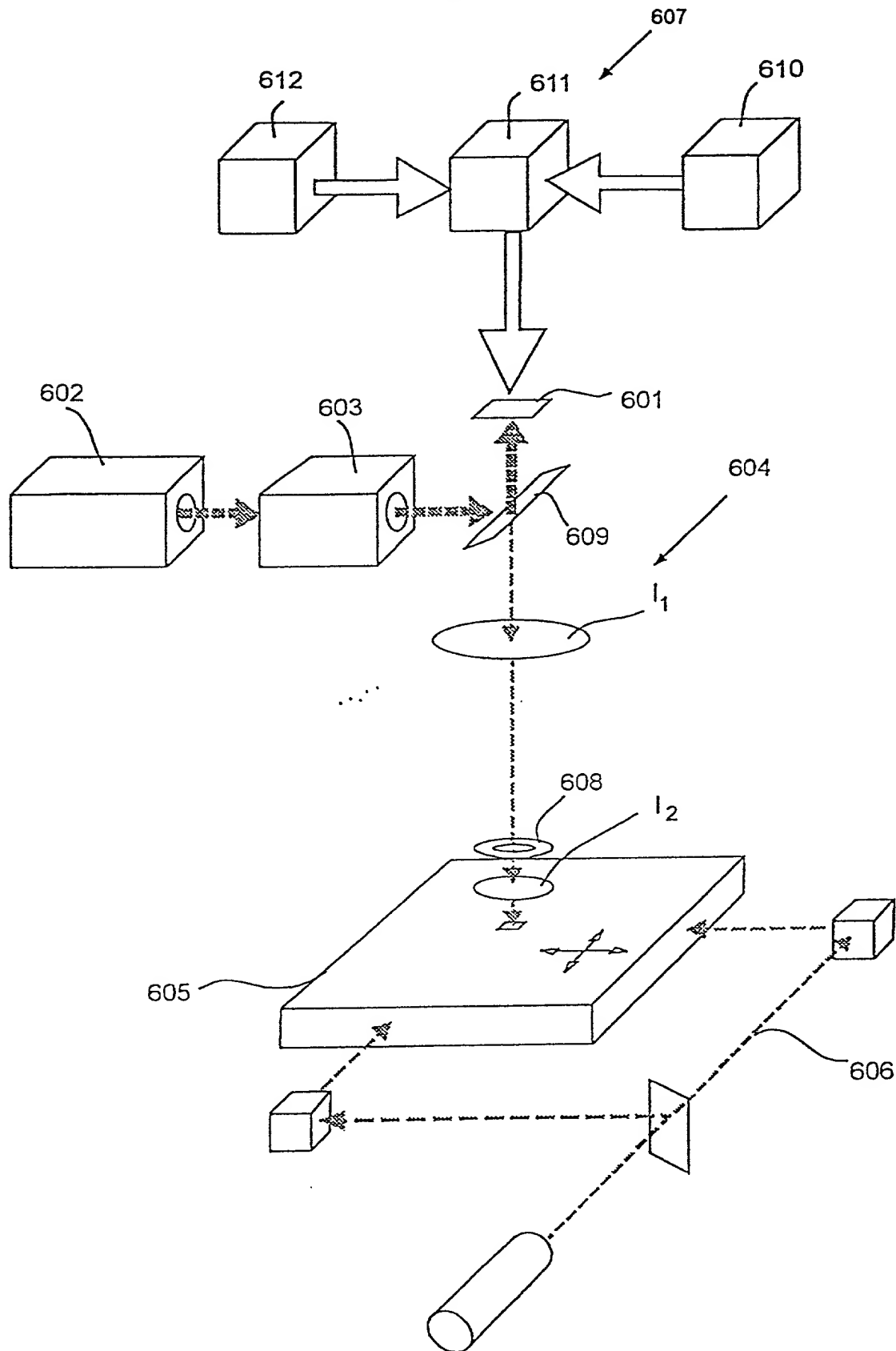


Fig. 6

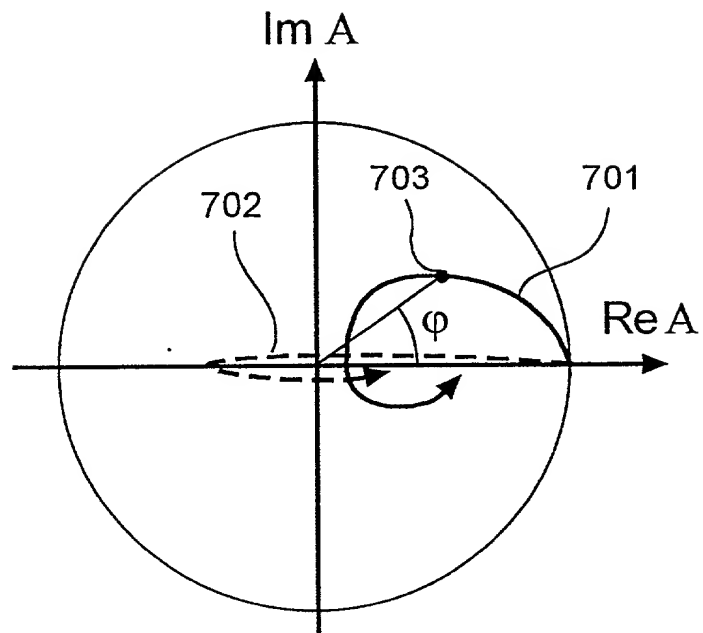
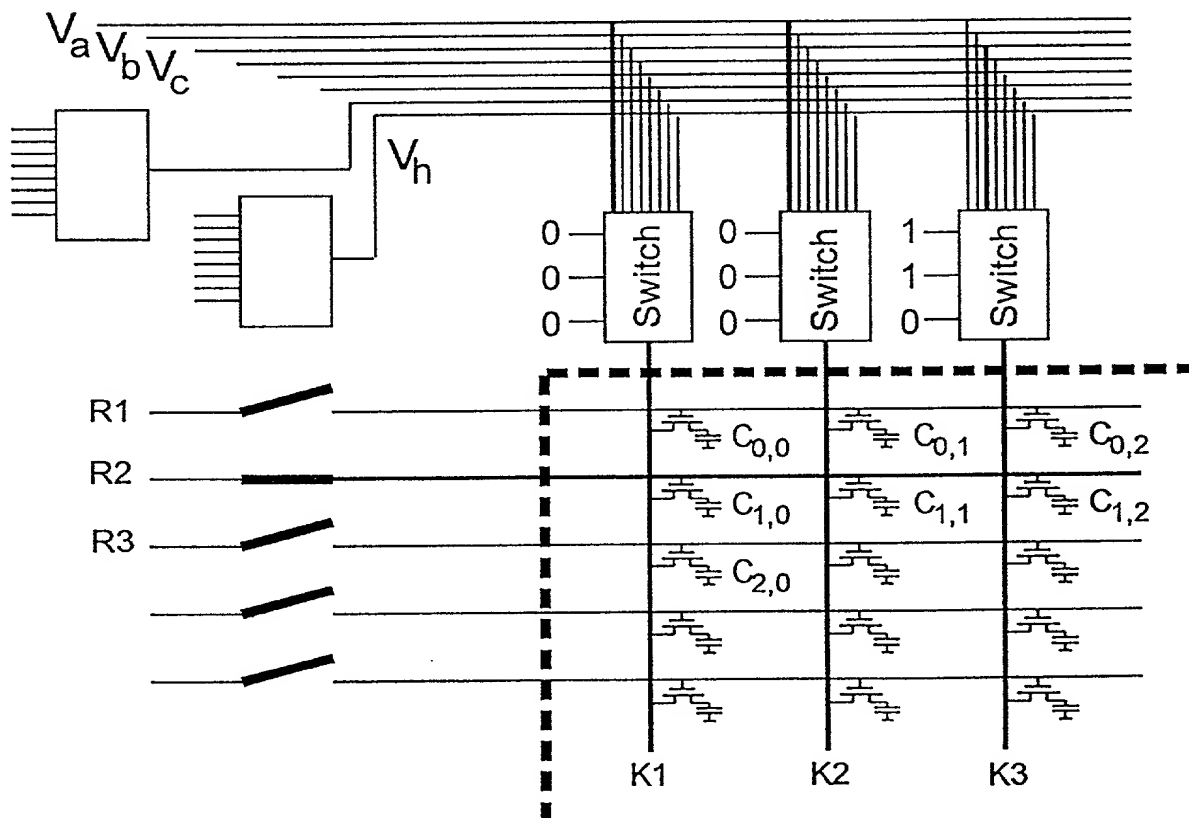
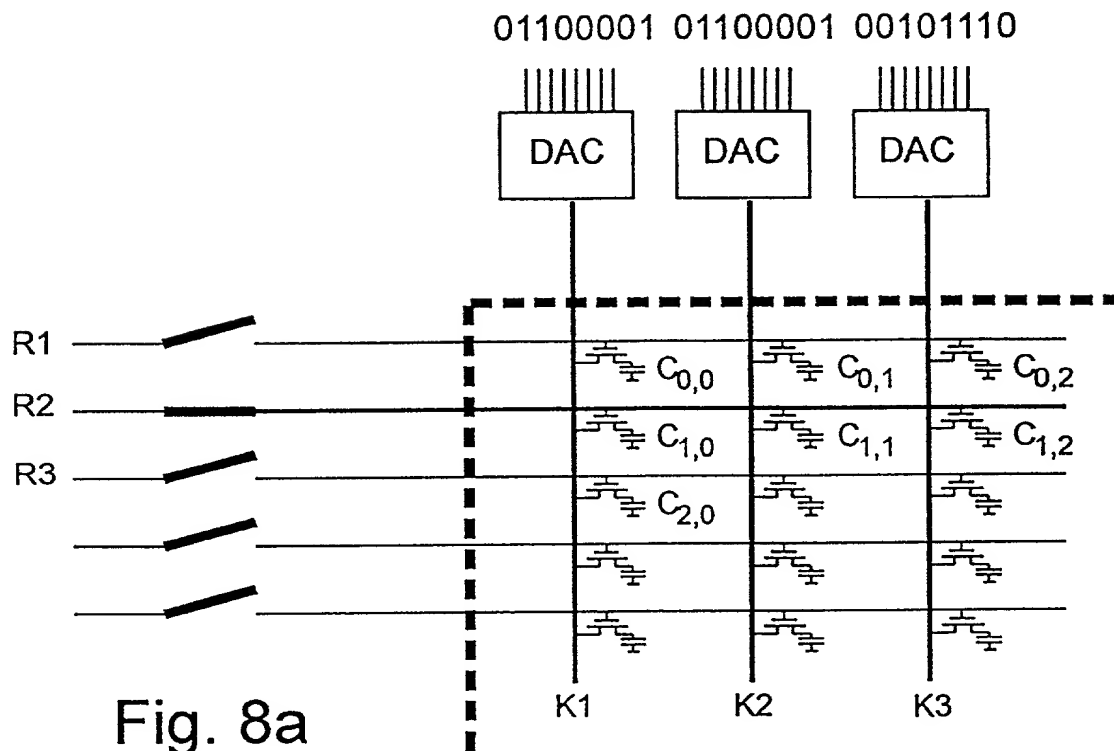


Fig. 7



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FOLLOWING**COMBINED DECLARATION AND POWER OF ATTORNEY
FOR PATENT AND DESIGN APPLICATIONS**

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated next to my name; that I verily believe that I am the original, first and sole inventor (if only one inventor is named below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

Insert Title:

IMPROVED PATTERN GENERATORFill in Appropriate
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Specification
Attached:

the specification of which is attached hereto. If not attached hereto,

the specification was filed on _____ as

United States Application Number _____ ;

and amended on _____ (if applicable) and/or

the specification was filed on March 2, 1999 as PCTInternational Application Number PCT/SE99/00310 ; and was

amended under PCT Article 19 on _____ (if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56. I do not know and do not believe the same was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representative or assigns more than twelve months (six months for designs) prior to this application, and that no application for patent or inventor's certificate on this invention has been filed in any country foreign to the United States of America prior to this application by me or my legal representatives or assigns, except as follows.

I hereby claim foreign priority benefits under Title 35, United States Code, §119(a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed

Insert Priority
Information:
(if appropriate)9800665-3
(Number)Sweden
(Country)March 2, 1998
(Month/Day/Year Filed)☒
Yes☐
No

(Number)

(Country)

(Month/Day/Year Filed)

☐
Yes☐
No

(Number)

(Country)

(Month/Day/Year Filed)

☐
Yes☐
No

(Number)

(Country)

(Month/Day/Year Filed)

☐
Yes☐
No

I hereby claim the benefit under Title 35, United States Code, §119(e) of any United States provisional application(s) listed below.

Insert Provisional
Application(s):
(if any)

(Application Number)

(Filing Date)

(Application Number)

(Filing Date)

All Foreign Applications, if any, for any Patent or Inventor's Certificate Filed More than 12 Months (6 Months for Designs) Prior to the Filing Date of This Application:

Country

Application Number

Date of Filing (Month/Day/Year)

Insert Requested
Information:
(if appropriate)

I hereby claim the benefit under Title 35, United States Code, §120 of any United States and/or PCT application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States and/or PCT application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information which is material to the patentability as defined in Title 37, Code of Federal Regulations, §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

Insert Prior U.S.
Application(s):
(if any)

(Application Number)

(Filing Date)

(Status - patented, pending, abandoned)

(Application Number)

(Filing Date)

(Status - patented, pending, abandoned)

I hereby appoint the following attorneys to prosecute this application and/or an international application based on this application and to transact all business in the Patent and Trademark Office connected therewith and in connection with the resulting patent based on instructions received from the entity who first sent the application papers to the attorneys identified below, unless the inventor(s) or assignee provides said attorneys with a written notice to the contrary:

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Inventor, if any:
see above

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GIVEN NAME/FAMILY NAME		INVENTOR'S SIGNATURE	DATE*
Residence (City, State & Country)		CITIZENSHIP	
POST OFFICE ADDRESS (Complete Street Address including City, State & Country)			

*DATE OF SIGNATURE